



PATENT

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

Andrew Strachan et al.

Application No.: 10/659,421

Filed: September 10, 2003

For: METHOD OF IMPROVING THE
BREAKDOWN VOLTAGE OF A
DIFFUSED SEMICONDUCTOR
JUNCTION

Confirmation No.: Unknown

Group Art Unit: Unknown

Examiner: Unknown

SUBMISSION OF FORMAL DRAWINGS

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Commissioner for Patents
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I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope, addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on October 3, 2003.

STALLMAN & POLLOCK LLP

Dated: 10/03/03

By: 

Lana T. Brenner

Sir:

Enclosed are three (3) sheets of formal drawings for filing in the above-referenced application.

Respectfully submitted,

STALLMAN & POLLOCK LLP

Dated: October 3, 2003

By: 

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